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FIG.1A

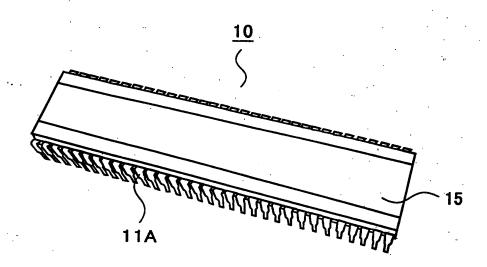


FIG.1B

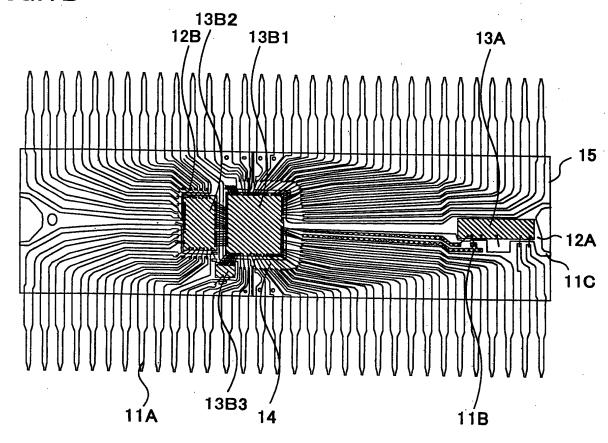


FIG.2A

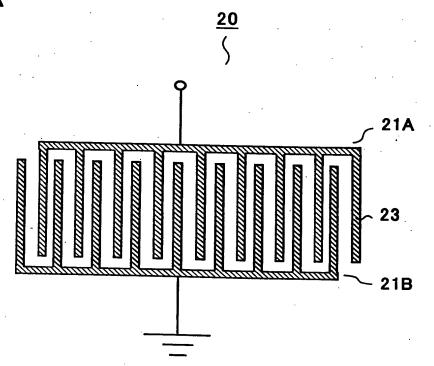
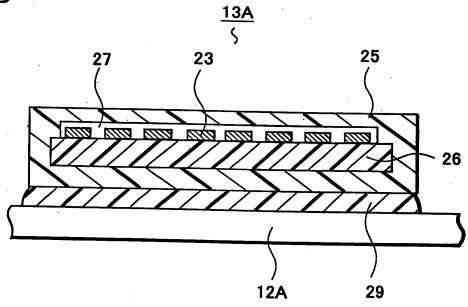
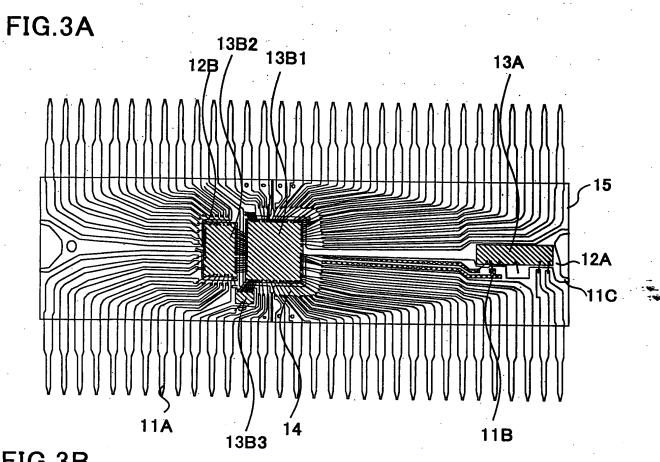
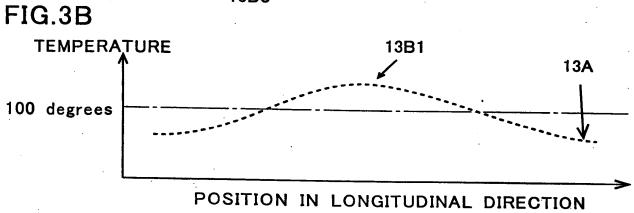


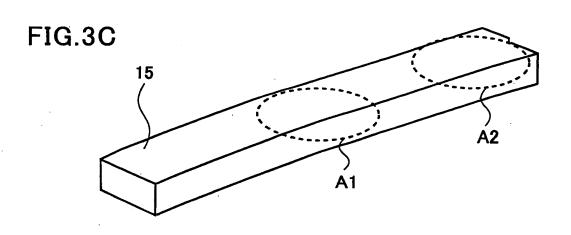
FIG.2B



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FIG.4

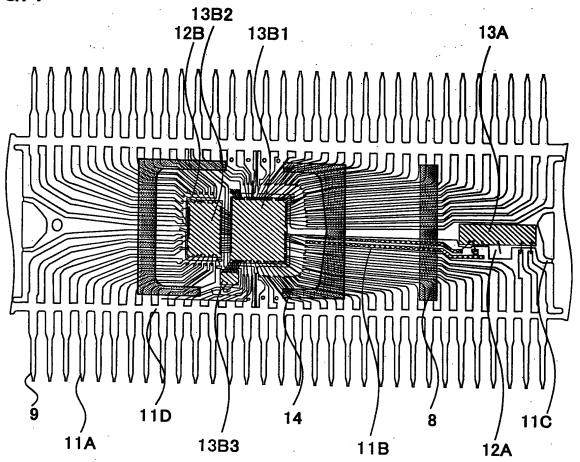


FIG.5A

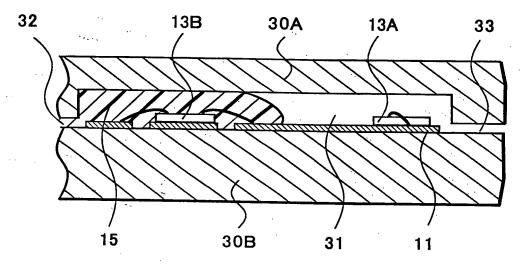


FIG.5B

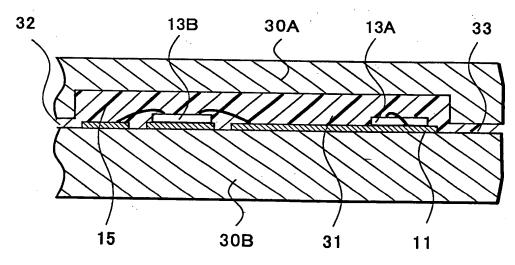
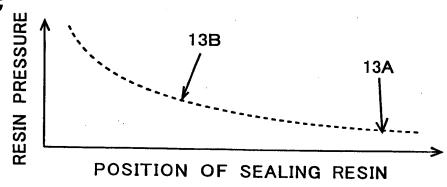


FIG.5C



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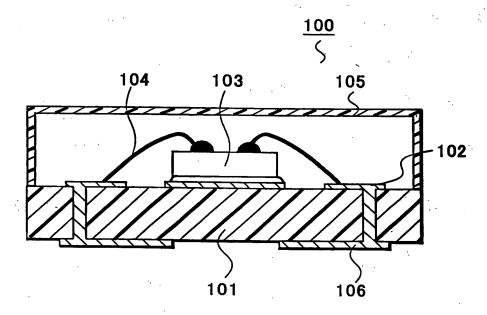


FIG.7

